

Features:

- Proprietary Graphical User Interface (GUI) Software
- Ethernet Control, Independent of Audio Inputs
- Analog and AES Digital inputs
- Analog and Digital Loop-through Outputs
- Digital Loop-through Follows Analog Too
- Emergency Signal Mode, Analog Overrides Digital
- 1 in, 3 Out
- Monitors and Displays for All Three Channels
 - Amplifier Current
 - Amplifier Temperature
 - Amplifier Protect
 - Amplifier Clip
- Manual Controls (Knobs) Via Serial Bus
- Full range 20 to 20kHz Bandwidth
- SNR Digital In Analog Out 91dB
- Digital Noise Voltage 12uV (9.2uV A-weighted)
- Analog Noise Voltage 30uV (15uV A-weighted)
- Compatible with A-Series Amplifiers and P-Series Power Supplies
- Dimensions:
 - inches: 3.75 x 2.85 x 1.5
 - mm: 95 x 72 x 38



Product Description: **New for 2008!**

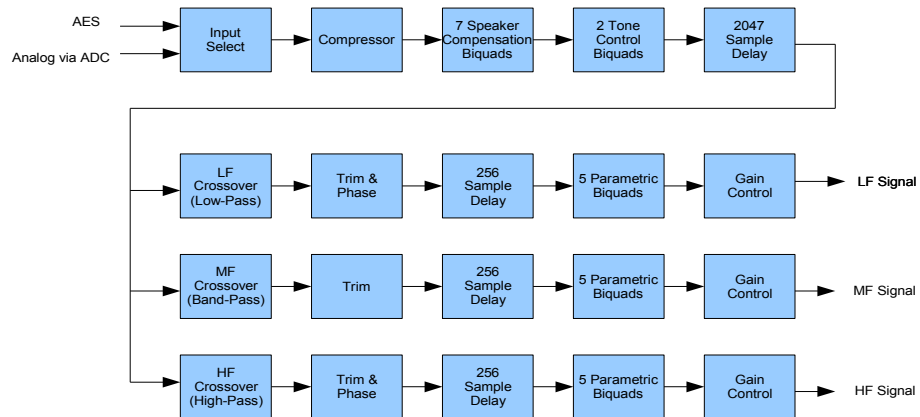
The DSPn™ is PowerPhysics' giant step into the digital world. An Ethernet controlled preamplifier with a proprietary Graphic User Interface (GUI) whose features meet the needs of speaker manufacturers, system consultants and end users.

Each speaker is individually controlled in real time using standard ethernet CAT-5 wiring and routing hardware. The GUI monitors all the speakers in the system. You know the health and status of every amplifier for total peace of mind. Audio signals are kept separate and use XLR connectors for analog or AES digital inputs. Once configured, your system will run without the ethernet control, in keeping with PowerPhysics mandate to product the most robust products available.

Crossovers and tuning filters are also part of the package. In conjunction with your measurement system, the DSPn™ lets you tune and align your speaker in real time with more control.

The DSPn™ continues in PowerPhysics tradition of elegant design. The single board DSP solution mounts to the top of the Compact Series heat sink to create a complete audio solution. PowerPhysics provides all the electronics you need to build a state of the art active speaker.

DSP-n Signal Processing Block Diagram



DSPn System Physical Block Diagram

